

FIG. 1

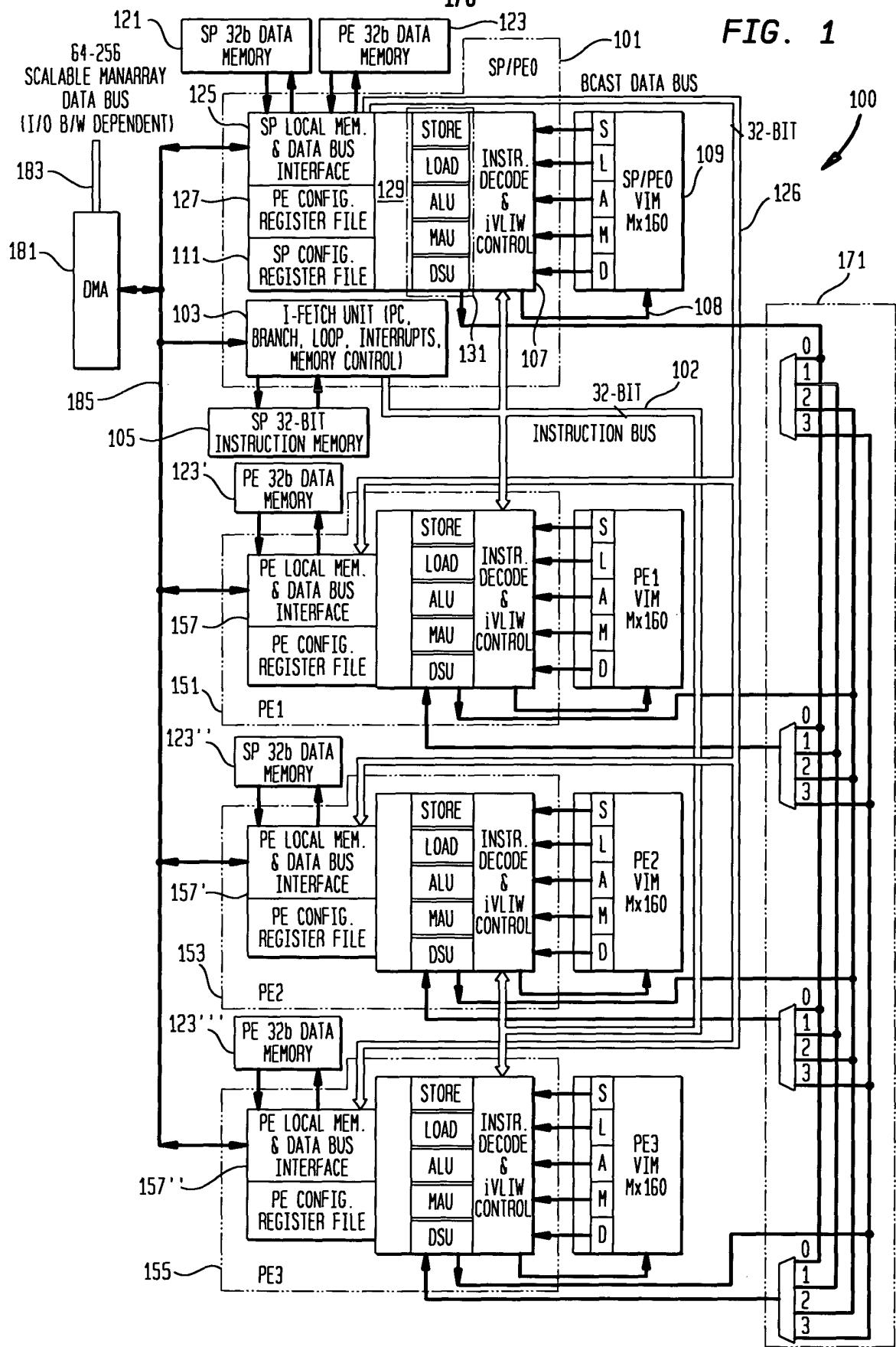


FIG. 2A

Group	S/P	Unit	AluOpcode	CMPCC	0	CC	Rx	RY	RxE	RyE	0	Combo	Dpack
31	30	29	28	27	26	25	24	23	22	21	20	19	18

200

FIG. 2B

CC	Description	C-N-V-Z Setting
Z(EQ)	Zero or Equal	Z=1
NZ(NE)	Not Zero or Not Equal	Z=0
H1	Higher (unsigned)	(C=1) & (Z=0)
H1(CS)	Higher or Same (unsigned, or Carry Set)	C=1
L0(CC)	Lower (unsigned, or Carry Clear)	C=0
LS	Lower or Same (unsigned)	(C=0) (Z=1)
VS	Overflow Set	V=1
VC	Overflow Clear	V=0
POS	Positive	(N=0) & (Z=0)
NEG	Negative	N=1
GE	Greater-than or Equal (signed)	N=V
GT	Greater-Than (signed)	(Z=0) & (N=V)
LE	Less-than or Equal (signed)	(Z=1) (N!=V)
LT	Less-Than (signed)	N!=V

202

204 205 206 207 208

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FIG. 3A

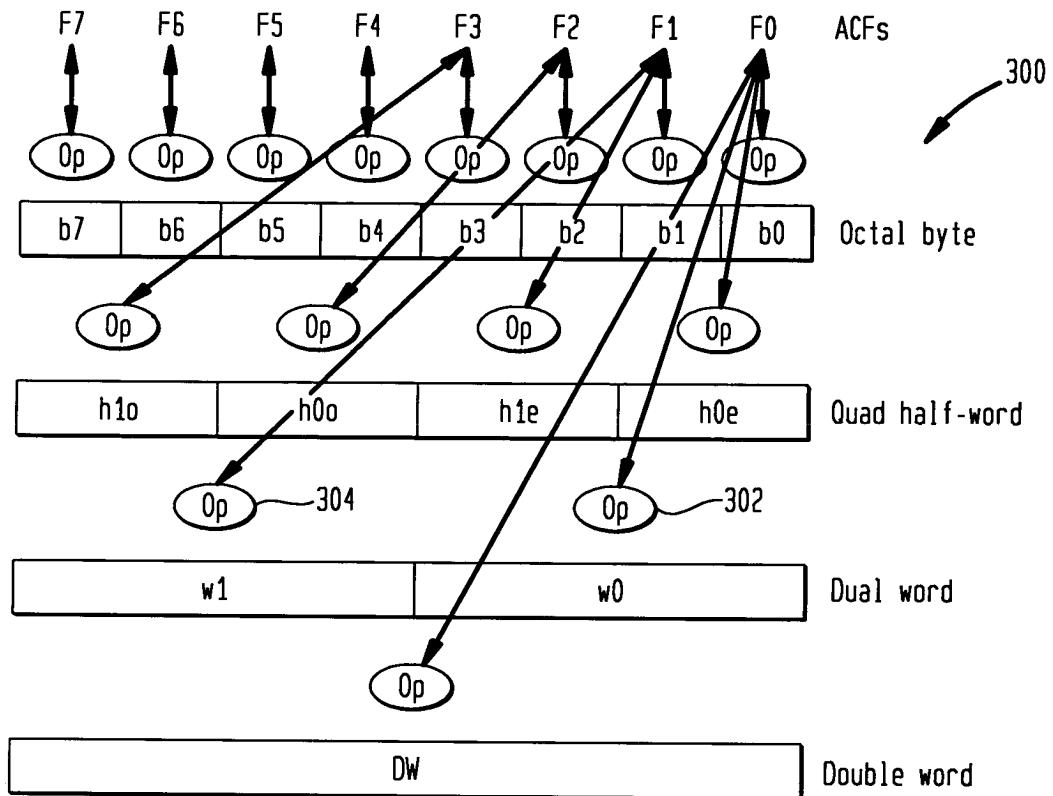


FIG. 3B

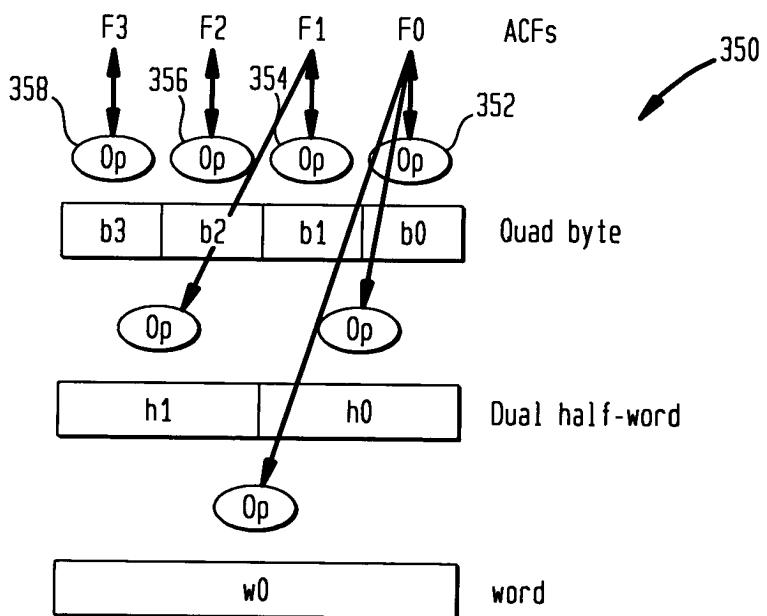


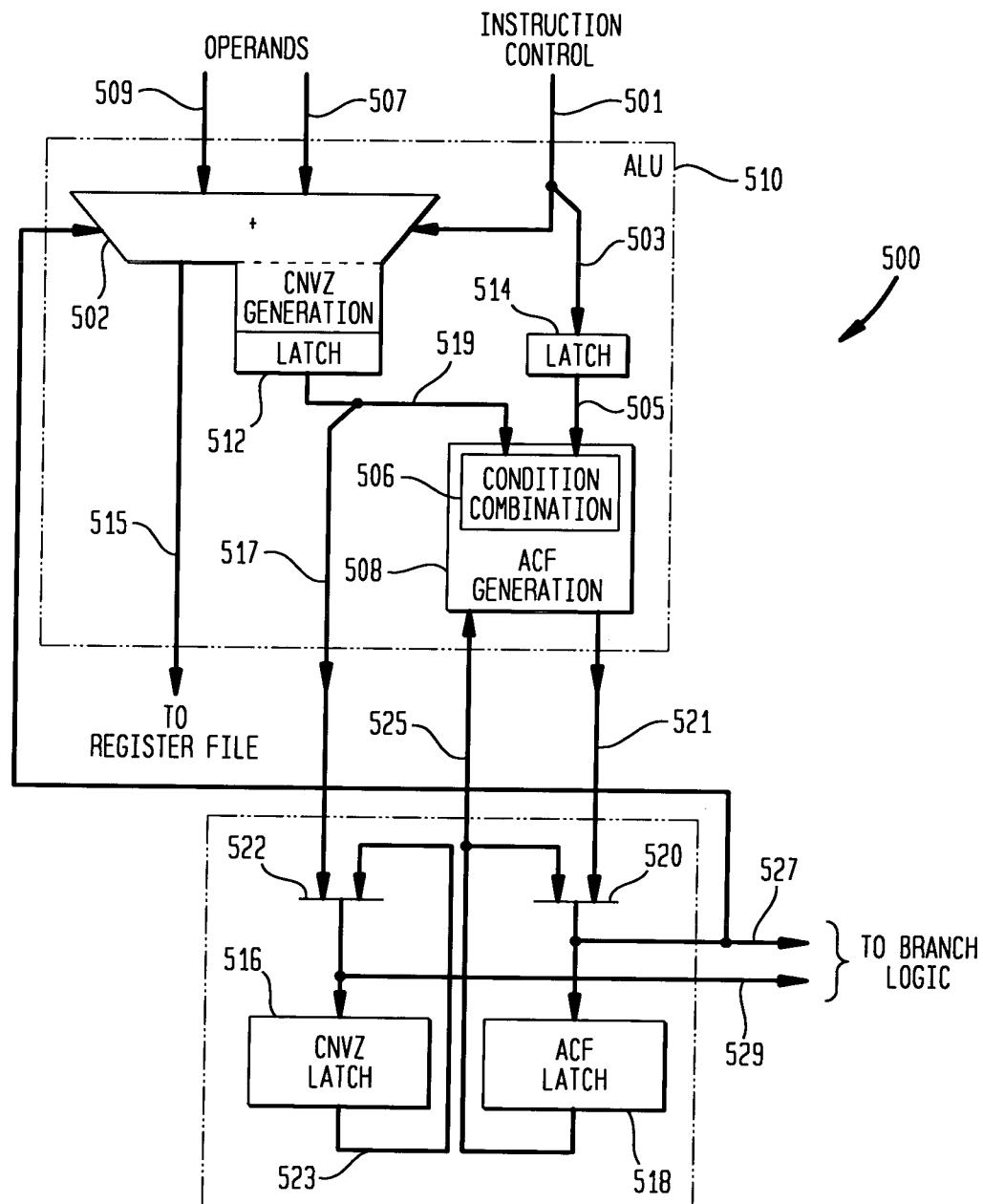
FIG. 4A

FIG. 4B

FIG. 4C

Logic Instruction Encoding																															
			420									422																			
			Allopcode			Logic Operation			Rx			By																			
Group	SI	P	Unit	Al	Opcode	Op	Op	Op	Rx	Rx	Rx	CE3b	LogicExt																		
31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0

FIG. 5A



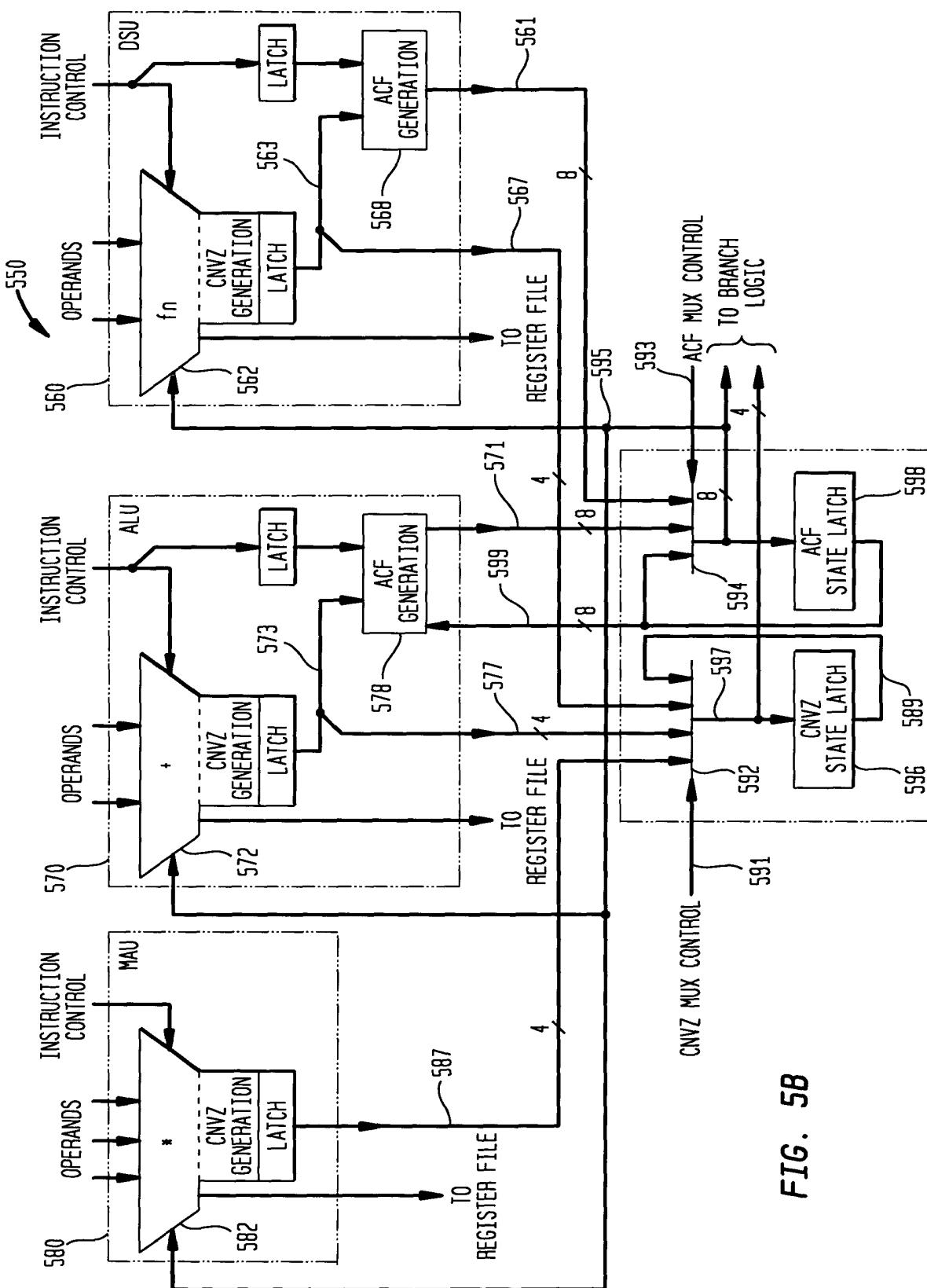


FIG. 5B